PATENT

C. M. Sublicia C. C. C. C. C. MAR 2005

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In re application of).	Art Unit:					
Inventor:	Peter J. Hopper)	At out.					
Serial No.:	10/716,276)	Examiner:					
Filed:	11/17/2003)						
Title:	METHOD OF ETCHING A)						
LATERAL TRENCH UNDER AN EXTRINSIC)								
BASE AND IMPROVED BIPOLAR								
TRANSISTOR								
Commissioner for Patents Washington, D.C. 20231 Request for correction of Filing Receipt Please correct the name of Peter Johnson as shown in the attached copy of the filing receipt.								
	Respectfully Submitted,							
Dated:	3/ 15 /, 2005		& Associates r Street #531					

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Jurgen Vollrath 588 Sutter Street #531 San Francisco, CA 94102 CONFIRMATION NO. 6046
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Date Mailed: 02/04/2005

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Applicant(s)

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Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 02/18/2004

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is US10/716,276

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

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Early Publication Request: No

Title

Method of etching a lateral trench under an extrinsic base and improved bipolar transistor

Preliminary Class

257

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